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S Kundu - Test Conference, 1998. Proceedings. International, 1998 - ieeexplore.ieee.org
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... and Time Zone Changes **Static timing** analyzers characteristically ... slave clock, propagated
through **combinatorial** logic, and ... a stream of dynamic **domino** logic is ...
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D Van Campenhout, T Mudge, K Sakallah... - eecs.umich.edu
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